

Application Serial No. 10/642,936
Reply to Office Action of March 2, 2005

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MAR 23 2005

PATENT
Docket: CU-3332

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application: Naoyuki Koizumi } GRP ART UNIT: 2813
Serial No: 10/642,936 } Ex.: Doty, Heather
Filed: August 18, 2003 }

For: Semiconductor Chip and Fabrication Method Thereof

Certification under 37 C.F.R. §1.8(a)

The USPTO Central Fax No. (703) 872-9306

Date of Fax Transmittal: March 23, 2005

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being transmitted to the United States Patent and Trademark Office to the fax number and on the date indicated above.


W. William Park, Reg. No. 55,523

The Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

In the Office Action dated March 2, 2005, setting a 1-month shortened statutory period for a reply ending on April 2, 2005, the pending Claims 1-6 were restricted as follows:

Claims 1-5 drawn to method; and

Claims 6-8 drawn to device.

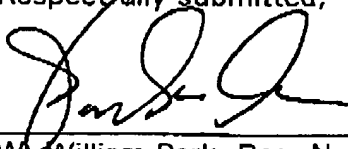
Applicant hereby elects Claims 1-5.

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In view of the Examiner's earlier restriction requirement, Applicants retains
the right to present the non-elected in a divisional application.

Respectfully submitted,



Dated: March 23, 2005

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